

Schedule of Class Activities

Lecture	date	Chapter	Omit Sec	Homework Problems
1	Tu 1/08	1		
2	Th 1/10	1		ch01: 10,11,27,32,38,47,49
3	Tu 1/15	2	7,8	ch02: 2,11,22,33,40,43,66,68
4	Th 1/17	3	6,7	ch03: 10,22,32,43
5	Tu 1/22	4		
6	Th 1/24	4		ch04: 19,20,28,30,31,45,48
7	Tu 1/29	5		
8	Th 1/31	5	7	ch05: 7,18,22,26,27,32,41,43
9	Tu 2/5	6		
10	Th 2/7	6	8	ch06: 17,26,27,46,47,61
11	Tu 2/12			Exam, Chap. 1-6
12	Th 2/14	10		
13	Tu 2/19	10		
14	Th 2/21	10	3	ch10: 1,2,4,33,34,55,61
15	Tu 2/26	11		ch11: 1,2,13,14
16	Th 2/28	12		
17	Tu 3/4	12		ch12: 1,2,4,20,31,41,46 Withdrawal Deadline
18	Th 3/6	13		
	3/10 -	3/14		Springbreak
19	Tu 3/18	13		
20	Th 3/20	13		ch13: 1,2,3,19,41,42,46,61
21	Tu 3/25			Exam, Chap. 10-13
22	Th 3/27	14		
23	Tu 4/1	14		ch14: 8,9,10,17,27,57,58
24	Th 4/3	07		
25	Tu 4/8	07		
26	Th 4/10	07		ch07: 2,4,5,7,10,18,19,20,21,23,53,57
27	Tu 4/15	09		ch09: 10,24,25,26
28	Th 4/17			
29	Tu 4/22			Exam, Chap. 7,9,14
30	Th 4/24			Review
	Th 5/01			**Final Exam 3:30pm-6:30pm**

Course Grading: Hourly Exams-45%, Laboratory-20%, Final Exam-20%, Homework-15%

Letter grades: A 90.0, A- 87.5, B+ 85.0, B 80.0, B- 78.5, C+ 75.0, C 70.0, C- 67.5, D 60.0

Textbook: Electrical Engineering: Principles and Applications, 3rd edition by A.R. Hambley, Pearson-Prentice-Hall

Chap. 1,2: DC circuits, Thevenin's and Norton's theorems

Chap. 3,4: Transient analysis

Chap. 5: AC circuits, complex variables

Chap. 6: Frequency response of circuits

Chap. 7: Logic circuits and gates: Boolean algebra and K-maps

Chap. 9: Computer based instrumentation: Analog to digital conversion

Chap. 10: Semiconductor physics: p-n junction and diodes in circuits

Chap. 11: Basic amplifiers

Chap. 12: Field-effect-transistors

Chap. 13: Bipolar-junction-transistors

Chap. 14: Operational amplifiers

The purpose of the course is to study the basic fundamentals of circuit analysis. You are not going to build a computer or stereo, but you should have an understanding of how logic circuits and amplifiers work. The course material falls into two categories: Electrical circuits and electronics. One must understand the behavior of electrical circuits in order to understand electronics. The first five weeks (Chap. 1-6) cover electrical circuits. These are circuits that contain only passive elements, namely resistors, capacitors, and inductors. The remaining ten weeks deal with circuits containing active elements, such as diodes, transistors, op-amps, and logic gates.

Homework is due the class period after the chapter has been covered in lecture. For example, chap. 2 solutions are due on Thursday 1/20, the day we start chap. 3 (see the schedule above). Homework solutions will be posted on the web after they have been turned in:

www.physast.uga.edu/classes/phys3320/meyer/index.html

Examinations are one hour in-class exams, except for the final, and are open-book. Warning: Too often I see students wasting a lot of time searching for material in the book. Treat each exam as closed-book and use the open-book option sparingly.

The laboratory is a very important part of understanding electronics. It meets in room 324A of the Physics Building. As you will discover, it is one thing to see a schematic of a circuit and analyze it, and it is another to build the circuit and make it work. Experience counts. Laboratory reports will have a different theme each week. I have tried to imagine different reasons for writing about your laboratory exercises. Most of them have to do with reports that you might be asked to write as someone working in industry. Imagination helps in writing laboratory reports.

I don't keep formal office hours. Feel free to come by my office (Room 223B) or lab (Room 102) at anytime. If I am not around, you can try contacting me by phone (542-2020) or by email hmeyer@uga.edu.